

OLMS63K SERIES 4-bits Low Power MCU CHECK LIST

Device Name: MSM63238-

(1) PACKAGE

- 80-pin Form : GS-BK 100-pin Form : TS-K Chip Form :

(2) VOLTAGE SUPPLY

- When Back up is used (VDD=0.9V to 2.7V)
Upon reset, the BACKUP bit is set to "1" to enter the back up state.
- When Back up is not used (VDD=1.8V to 5.5V)
To release the back up state, the BACKUP bit should be reset "0" .
Connect VDD to VDDH externally.

(3) BATTERY LOW DETECTION CIRCUIT (BLD)

- Enable BLD only when battery check is carried out.
- Read the BLDF flag 10ms or more after setting the ENBL to "1" .

(4) HIGH SPEED CLOCK OSCILLATOR CIRCUIT

- RC Oscillator Mode (OSCSEL=0)
TWAIT=6ms or longer after ENOSC=1
- Ceramic Oscillator Mode (OSCSEL=1)
TWAIT=10ms or longer after ENOSC=1
- When changing the High-speed clock to Low-speed one, reset CPUCLK to "0"
first and after that reset ENOSC to "0" . Never reset both at the same time, and
follow the above turn.

(5) LOW SPEED CLOCK OSCILLATOR CIRCUIT

- The XTSEL0/1 port to select low speed clock frequency should be connected
to "VDD" or "VSS" .

(6) USABLE ROM SIZE

- 16384 ×16 bits

(7) USABLE STACK SIZE

- Call stack : 16 levels
Register stack : 16 levels

(8) INITIALIZATION OF RAM

- RAM content is undefined after power up - remember to do initialization.

(9) INPUT PORTS AND I/O PORTS

- When selecting High impedance input, the port should be connected to "VDD"
or "VSS" .